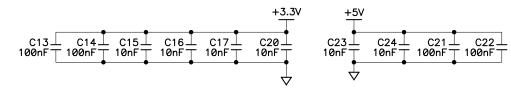
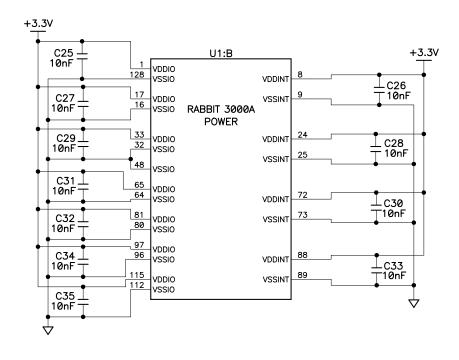
## STUFFING TABLE

CIRCUIT	PART	RCM3700 RCM3710		RCM3720
SRAM SRAM select	U2 JP2	512K 2-3	128K 1-2	256K 1-2
FLASH 1 FLASH 1 select	U3 JP3	512K 2–3	256K 1-2	512K 2-3
BANK SELECT	JP1	1-2	1-2	1-2
POWER TO VRAM	R7	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
INTERRUPT OPTION	R28	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
RTC	U4	INSTALLED	INSTALLED	INSTALLED
1110	R4	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
SERIAL FLASH	U9	INSTALLED	INSTALLED	INSTALLED
SERVICE PERSON	U13	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED

## **DE-COUPLING CAPS**



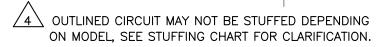
REVISION HISTORY			REVISION APPROVAL					
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE		
		PRE-PRODUCTION RELEASE REV A, B						
С	E12348	INITIAL RELEASE	DM	8/1/03	KF	8/1/03		
D	E12504	CHANGED RCM3710 SRAM	DM	10/24/03	KF	10/24/03		
E	E12695	CORRECT FLASH SIZE FOR RCM3710	EP	3/10/04	KF	3/10/04		
F	E12880	ADDED RCM3720 STUFFING OPTION	TCP	8/13/04	KF	8/12/04		
G	E13209	REPLACED T1 TO 10/100BASE—T. CHANGED VALUE OF R1, R8, R9, R30, R32. ADDED FERRITE BEAD (L5) AT J3. UPDATED STUFFING CHART.	ХТ	4/22/05	KF	4/22/05		
Н	1E141111	INSTALL SCHMITT TRIGGER AT U4. REMOVE RESISTOR AT R4.	XT	9/30/05	KF	9/28/05		
J	E15144	CHANGED Y2 TO CRYSTAL						
K	E15518	CHANGED T1, U9 AND JP1-3; ADDED TP11						



NOTES: UNLESS OTHERWISE SPECIFIED;

- 1. ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
- 2. ALL CAPACITORS ARE 10VDC OR HIGHER.
- 3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY ( vcc ), AND ALL REFERENCES TO THAT VOLTAGE

  ARE REPRESENTED BY ( vcc ).



5. COMPONENT VALUES SHOWN WITH AN ASTERISK (\*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL.

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COF INIGHT 2007, NABBITSLIMICONDUCTOR, INC.											
APPEND THE FOLLOWING		DRAWING CONTENT:		TITLE							
DOCUMENTS W	HEN CHANGING	DRAWN BY: (INITIAL RELEASE)									
THIS DOCUMENT:		DARREN MUSGROVE	6/12/03			TIO		4 5	4 7	<b>.</b> .	
		REVISED BY:		15CH	1∟M/	ATICL	DIAGRAN	$A \mid K$	AI	B	II
		J. NAUER	11/07/07					SEN	<i>IICO</i>	NDU	$\overline{C}T\overline{O}R$
		U. IVACEIX	11/27/07		R	11/137/	<b>え</b> ぬ				
		APPROVALS: INITIAL RELEASE		RCM3700							
		PROJECT ENGINEER:						ww	w.ra	abbit	t.com
				SIZE	DWG NO.						
		ENGINEERING MANAGER:		В	(	290-	-0177				
					`						
		SIGNATURES	DATE	SCALE	NONE	RELEASE DATE	6JUN03	SHEET	1	OF	3

